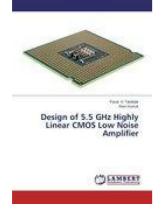
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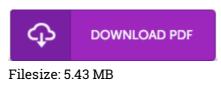
DESIGN OF 5.5 GHZ HIGHLY LINEAR CMOS LOW NOISE AMPLIFIER



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- Authored by Fazal A. Talukdar
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